

**S/N 10/643,585**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Steven L. Scott	Examiner:	Sheng Tsai
Serial No.:	10/643,585	Group Art Unit:	2186
Filed:	August 18, 2003	Docket No.:	1376.700US1
Customer No.:	21186	Confirmation No.:	4004
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER		

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**COMMUNICATION RE: FEE ADDRESS**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

In response to the Notice of Allowance and Issue Fee Due, please record the Fee Address under the provisions of 37 CFR 1.363 as the following:

**Customer Number 21186**

Please direct any inquiries to the undersigned attorney at (612) 373-6909.

Respectfully submitted,

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.  
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Date April 21, 2009

By Thomas F. Brennan  
Thomas F. Brennan  
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 21 day of April, 2009.

Maria C. Brothbach  
Name

[Signature]  
Signature